LESSON PLAN

BRANCH:- ELECTRIAL ENGG SEMESTER- 4TH SUB:AE Lab	FACULTY NAME: NIHARIKA SETHY , LECT- ETC SEMESTER START: FROM: 14.02.2023 TO -23 05.2023	
WEEK	NO OF CLASSES PER WEEK-3	TOPICS TO BE COVERED	STATUS
W1		Determine the input and output Characteristics of CE & CB transistor configuration	
W2		Determine Drain & Transfer Characteristics of JFET	
W3		Construct Bridge Rectifier using different filter circuit and to determine Ripple factor & analyze wave form with filter & without filter.	
W4		Construct Bridge Rectifier using different filter and to determine Ripple factor.	
W5		Construct & test the regulator using Zener diode	
W6		Construct different types of biasing circuit and analyze the wave form (i) Fixed bias (ii) Emitter bias (iii) Voltage divider bias	
W7		Study the single stage CE amplifier & find Gain	
W8		Study multi stage R-C coupled amplifier & to determine frequency- response & gain.	
W9		Construct & Find the gain (I) Class A. Amplifier (ii) Class B. Amplifier (iii) Class C Tuned Amplifier	
W10		Construct & test push pull amplifier & observer the wave form	
W11		Construct , draw wave form and calculate the frequency of (i) Hartley Oscillator	
W12		Construct , draw wave form and calculate the frequency of (ii) Colpitt Oscillator	
W13		Construct , draw wave form and calculate the frequency of (iii) Wein Bridge Oscillator	
W14		Construct , draw wave form and calculate the frequency of (iv) R-C phase shift oscillator and draw wave form & calculate the frequency	
W15		Construct & Test Differentiator and Integrator using R-C Circuit	
W16		Study of Multivibrators Circuit & Draw its Waveforms	