

BRANCH- ELE SEMESTER- 5 TH SUB: DE &MP LAB		LESSON PLAN	
		FACULTY NAME: NIHARIKA SETHY , LECT- ETC SEMESTER START: FROM: 01.08.2023 TO 30.11.2023 NO OF CLASSES ALLOTTED PER WEEK-3	
WEEK		TOPICS TO BE COVERED	STATUS
W1		Verify truth tables of AND,OR, NOT,NOR, NAND, XOR, XNOR gates.	
W2		Implement various gates by using universal properties of NAND & NOR gates and verify truth table.	
W3		Implement half adder and Full adder using logic gates	
W4		Implement half subtractor and Full subtractor using logic gates.	
W5		Implement a 4-bit Binary to Gray code converter	
W6		Implement a Single bit digital comparator	
W7		Study Multiplexer and de multiplexer.	
W8		Study of flip-flops. i) S-R flipflop ii) J-K flipflop iii) flipflop iv) T flipflop	
W9		Realize a 4-bit asynchronous UP/Down counter with a control for up/down counting.	
W10		Realize a 4-bit synchronous UP/Down counter with a control for up/down counting.	
W11		Implement Mode-1 0 asynchronous counters.	
W12		Study shift registers	
W13		a. 1'S Complement. b. 2'S Complement. a. Addition of 8-bit number. b. Subtraction of 8-bit number resulting 8/16 bit number. Decimal Addition 8-bit number. b. Decimal Subtraction 8-bit number a. Compare between two numbers.	
W14		b. Find the large stinan Array c. Block Transfer.	
W15		a) Traffic light control using 8255. b) Generation of square wave using 8255	