

BRANCH- ELE SEMESTER- 4 TH SUB:AE Lab		LESSON PLAN	
		FACULTY NAME: NIHARIKA SETHY , LECT- ETC SEMESTER START: FROM: 16 / 01 / 24 TO 26 / 04 / 24 NO OF CLASSES ALLOTTED PER WEEK-3	
WEEK		EXPERIMENTS TO BE CONDUCTED	STATUS
W1		Determine the input and output Characteristics of CE & CB transistor configuration	
W2		Determine Drain & Transfer Characteristics of JFET	
W3		Construct Bridge Rectifier using different filter circuit and to determine Ripple factor & analyze wave form with filter & without filter.	
W4		Construct Bridge Rectifier using different filter and to determine Ripple factor.	
W5		Construct & test the regulator using Zener diode	
W6		Construct different types of biasing circuit and analyze the wave form (i) Fixed bias (ii) Emitter bias (iii) Voltage divider bias	
W7		Study the single stage CE amplifier & find Gain	
W8		Study multi stage R-C coupled amplifier & to determine frequency- response & gain.	
W9		Construct & Find the gain (I) Class A. Amplifier (ii) Class B. Amplifier (iii) Class C Tuned Amplifier	
W10		Construct & test push pull amplifier & observe the wave form	
W11		Construct & calculate the frequency of (i) Hartley Oscillator and draw wave form & calculate the frequency	
W12		Construct & calculate the frequency of (ii) Collpitt Oscillator shift oscillator and draw wave form & calculate the frequency	
W13		Construct & calculate the frequency of (iii) Wein Bridge Oscillator (iv) R-C phase shift oscillator and draw wave form & calculate the frequency	
W14		Construct & Test Differentiator and Integrator using R-C Circuit	
W15		Study Multi vibrator (Astable, Bistable , Monstable) Circuit & Draw its Wave forms	